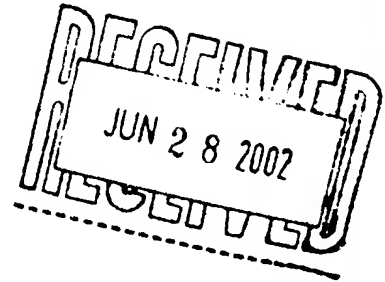


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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:) Group Art Unit: 2816
MAMORU UCHIDA)
Application No.: 10/025,928)
Filed: December 26, 2001)
For: SEMICONDUCTOR DEVICE,)
OPTOELECTRONIC BOARD,)
AND PRODUCTION METHODS)
THEREFOR : May 30, 2002



Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56 and in accordance with the practice under 37 C.F.R. §§ 1.97 and 1.98, the Examiner's attention is directed to the documents listed below and on the enclosed Form PTO-1449. Copies of the listed documents are also enclosed.

- (1) U.S. Patent No. 5,835,646
- (2) U.S. Patent No. 6,097,472
- (3) U.S. Publication No. 2001-0032984
- (4) Japan 59-75656
- (5) Japan 8-220357
- (6) Japan 9-96746
- (7) Japan 9-270751
- (8) Japan 10-294254
- (9) Japan 11-54406
- (10) Japan 11-72750
- (11) Japan 11-111609
- (12) Japan 11-196069
- (13) Japan 2000-31189
- (14) Japan 2000-31190
- (15) Japan 2000-235127
- (16) Japan 2000-337872

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(17) Japan 2001-284635

(18) "GaN FETs For Microwave and High-temperature Applications"

(19) "High-temperature Reliability of Gan Metal Semiconductor Field-Effect Transistor and Bipolar Junction Transistor"

Reference (15) was discussed at pages 2 to 3 of the specification, reference (8) at pages 18 and 29 of the specification, reference (9) at page 18 of the specification, reference (14) at page 27 of the specification, references (18) and (19) at page 60 of the specification, and reference (17) at page 65 of the specification. Reference (1) is believed to be an English-language counterpart of reference (6), reference (2) an English-language counterpart of reference (8), and reference (3) an English-language counterpart of reference (17). A partial English-language translation for reference (4) is enclosed. English-language translations for references (7) and (10) are enclosed. English-language abstracts for references (4) to (17) are also enclosed.

Inasmuch as the subject application has not yet received a first Office Action, it is believed that this Information Disclosure Statement is timely. See 37 C.F.R. 1.97(b)(3). Accordingly, the Examiner is urged to study this information in its entirety and to form an independent determination of the materiality of the information to the claimed invention.

It is respectfully requested that the above information be considered by the Examiner and that a copy of the enclosed Form PTO-1449 be returned indicating that such information has been considered.

Applicant's undersigned attorney may be reached in our Costa Mesa,
California office by telephone at (714) 540-8700. All correspondence should continue to
be directed to our address given below.

Respectfully submitted,


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